

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : **7,171,224 B2**
APPLICATION NO. : 09/832,051
ISSUE DATE : **January 30, 2007**
INVENTOR(S) : **Sarkkinen et al.**

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

The drawing sheet, consisting of Fig. 1, should be deleted to be replaced with the drawing sheet, consisting of Fig. 1 and Fig. 3 as shown on the attached pages.

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : 7,171,224 B1

Page 1 of 1

APPLICATION NO. : 09/832,051

ISSUE DATE : January 30, 2007

INVENTOR(S) : Sarkkinen et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

~~1. A system for processing data, comprising: a processor; a memory; and a computer readable medium encoded with instructions for the processor to execute, the instructions comprising: a first instruction for the processor to receive data from a source; a second instruction for the processor to process the data; and a third instruction for the processor to output the data to a destination.~~

In Claim 18, col. 16, line 65, after "means" insert -- encoded in a computer readable medium --.

In Claim 18, col. 16, line 65, after "executed" insert -- by a processor --.

In Claim 18, col. 16, line 65, delete the word "for."

In Claim 21, col. 18, line 63, after "components" insert -- encoded in a computer readable medium --.

In Claim 21, col. 18, line 63, after "executed" insert -- by a processor --.

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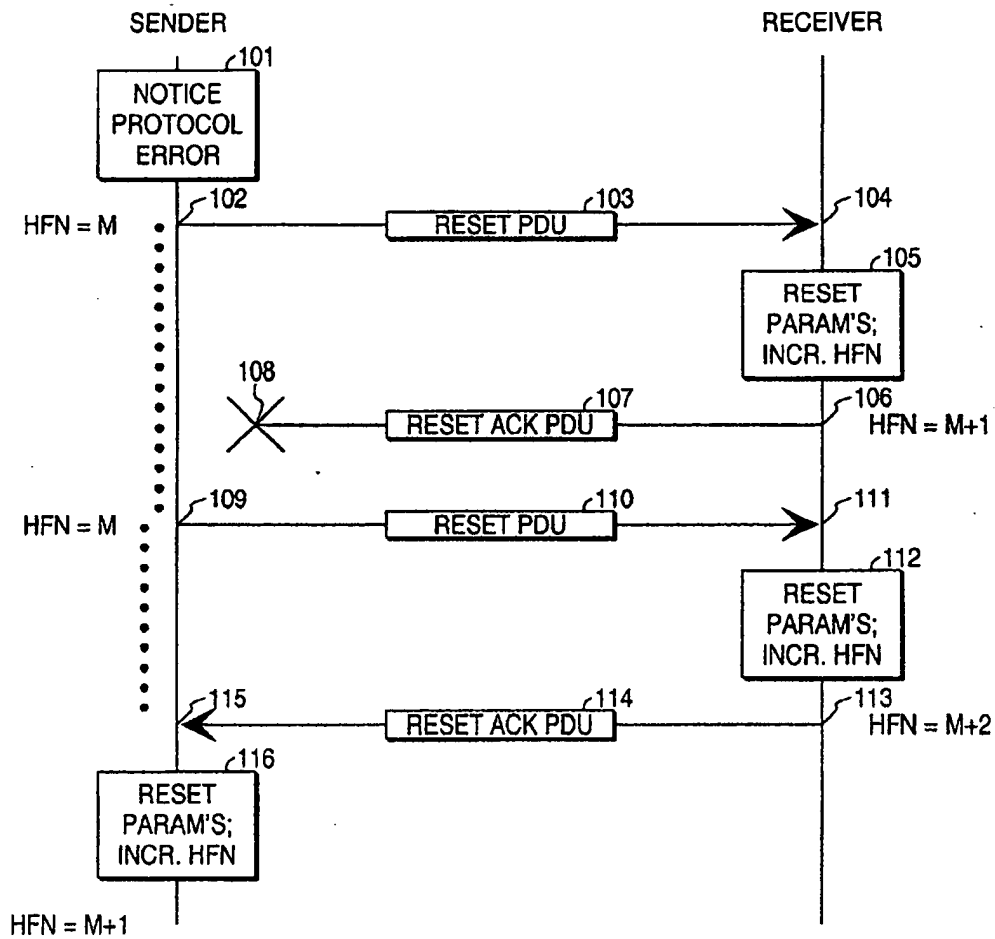


Fig. 1
PRIOR ART

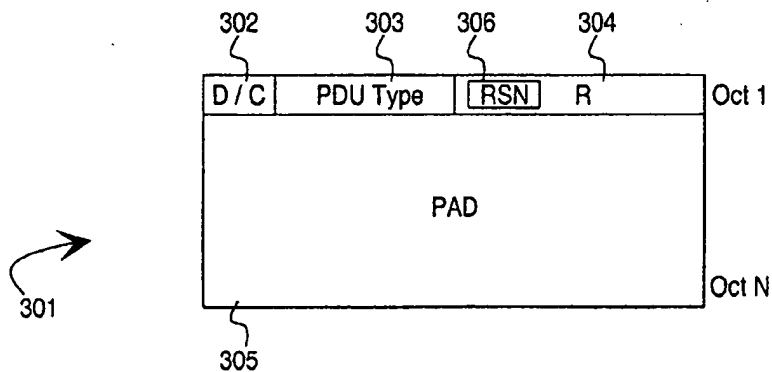


Fig. 3